

Time-domain data  
Sampling Frequency is 8kHz

Block diagram of FFT

Stage 1

Stage 2

Stage 3

Twiddle Factor.  
Twiddling Frequency is 1kHz

Multiplication of  $w[n]$  and  $a[n]$

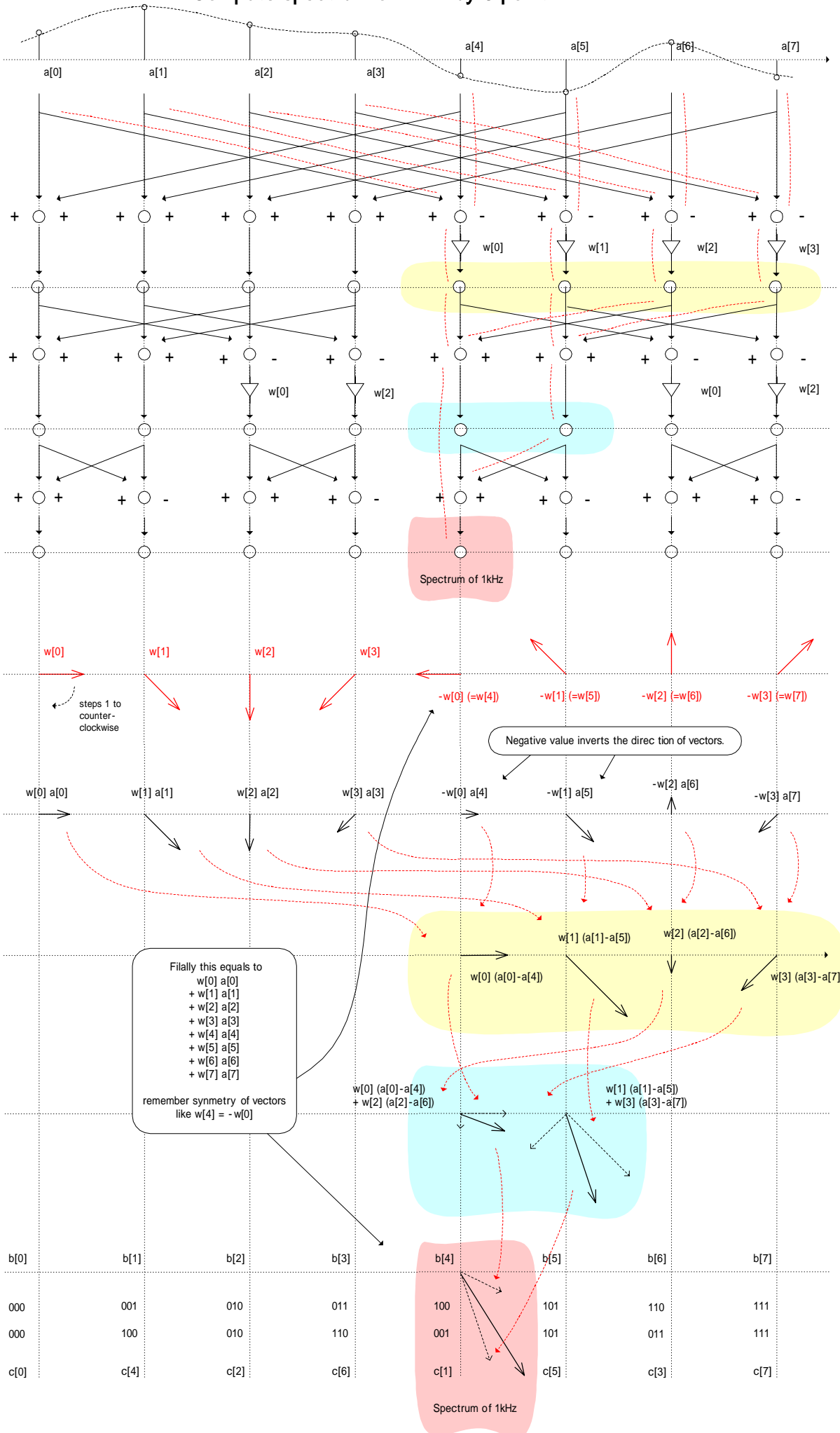
Stage 1

Stage 2

Stage 3

Bit - reverse order

Actual order of FFT



Finally this equals to

$$\begin{aligned}
 &w[0] a[0] \\
 &+ w[1] a[1] \\
 &+ w[2] a[2] \\
 &+ w[3] a[3] \\
 &+ w[4] a[4] \\
 &+ w[5] a[5] \\
 &+ w[6] a[6] \\
 &+ w[7] a[7]
 \end{aligned}$$

remember symmetry of vectors like  $w[4] = -w[0]$

Negative value inverts the direction of vectors.